

DLog	DLog	PIN	Func/	IO()	Port	RXD	TXD	ADC	DAC	
New	Old	#	Name	#	#	#	#	#	#	NOTES
										Green = requires Firmware 8.37
J1	J7	1	IO	4	1.0					Copy J10/17-3
J1	J7	2	IO	1	0.1					Copy J11/12-8
J1	J7	3	IO	0	0.0					Copy J11/12-6
J1	J7	4	TXD1	38	1.13		1			TXD1, UART1
J1	J7	5	IO	10	1.3					
J1	J7	6	TXD2	2	1.15		2			TXD2, UART2
J1	J7	7	IO	11	1.4					
J1	J7	8	IO	40	1.5					SW-D
J1	J7	9	IO	139	9.6					SW-C
J1	J7	10	IO	178	9.5					SW-B
J1	J7	11	IO	5	6.6					SW-A
J1	J7	12	IO	175	6.7					
J1	J7	13	TXD3	163	2.3		3			TXD3, UART3 (error, P2.2 in main table vs P2.3 in UART table & spec sheet)
J1	J7	14	RXD3	164	2.4	3				RXD3, UART3
J1	J7	15	IO	165	2.5					
J1	J7	16	IO	104	7.0					
J1	J7	17	WAKEUP	X	?					WAKEUP
J1	J7	18	CLKIN	X	F.4					CLKIN
J2	J8	1	IO	8	1.1					Copy J10/17-5; Pulled up during reset
J2	J8	2	IO	9	1.2					Copy J10/17-6; Pulled down during reset
J2	J8	3	CLK0	X	?					CLK0
J2	J8	4	IO	35	1.10					
J2	J8	5	RXD1	39	1.14	1				RXD1, UART1
J2	J8	6	IO	173	4.9					Copy J5/12-7
J2	J8	7	NC	X	X					Not Connected
J2	J8	8	IO	41	1.6					
J2	J8	9	GND	X	X					GND
J2	J8	10	IO	97	6.2					
J2	J8	11	IO	7	2.7					Copy J10/17-8; Pulled down during reset. Connected to LED and ISP BOOT pin
J2	J8	12	IO	167	2.8					Pulled down during reset
J2	J8	13	IO	102	6.10					
J2	J8	14	IO	14	2.10					
J2	J8	15	IO	70	4.6					Copy J11/12-3
J2	J8	16	-	X	3.0					Not used
J2	J8	17	VBAT	X	X					VBAT
J2	J8	18	RTC_A	X	X					RTC_ALARM
J3	J9	1	IO	174	4.10					Copy J10/17-1
J3	J9	2	IO	34	1.9					
J3	J9	3	IO	37	1.12					
J3	J9	4	IO	75	5.2					
J3	J9	5	IO	71	5.7					
J3	J9	6	IO	13	1.18					
J3	J9	7	IO	96	6.1					
J3	J9	8	TXD0	160	2.0		0			TXD0, UART0; Copy J11/12-2
J3	J9	9	RXD0	161	2.1	0				RXD0, UART0; Copy J11/12-1
J3	J9	10	NC	X	4.7					Not used
J3	J9	11	I2COC	X	X					I2COC
J3	J9	12	IO	101	6.9					
J3	J9	13	IO	42	2.9					Pulled down during reset
J3	J9	14	IO	44	2.12					
J3	J9	15	IO	105	7.1					
J3	J9	16	IO	107	7.3					
J3	J9	17	IO	66	4.2					
J3	J9	18	RESETn	X	X					RESETn
J4	J10	1	IO	73	5.0					Copy J10/17-2
J4	J10	2	IO	33	1.8					
J4	J10	3	IO	36	1.11					
J4	J10	4	IO	77	5.4					
J4	J10	5	RXD2	3	1.16	2				RXD2, UART2
J4	J10	6	CLKOUT	X	1.19					CLKOUT
J4	J10	7	-	X	6.0					Not used
J4	J10	8	IO	99	6.4					

J4	J10	9	IO	162	2.2					
J4	J10	10	IO	172	4.8					Copy J11/12-5
J4	J10	11	USB1P	X	X					USB1P
J4	J10	12	I2COD	X	X					I2COD
J4	J10	13	IO	103	6.11					
J4	J10	14	IO	43	2.11					
J4	J10	15	IO	168	3.1					
J4	J10	16	IO	106	7.2					
J4	J10	17	IO	68	4.4					
J4	J10	18	IO	110	7.6					
J5	J11	1	IO	74	5.1					Copy J10/17-4
J5	J11	2	IO	32	1.7					
J5	J11	3	IO	76	5.3					
J5	J11	4	IO	78	5.5					
J5	J11	5	IO	79	5.6					
J5	J11	6	IO	12	1.17					
J5	J11	7	IO	15	1.20					
J5	J11	8	IO	98	6.3					
J5	J11	9	IO	100	6.5					
J5	J11	10	IO	176	6.8					
J5	J11	11	USB1M	X	X					USB1M
J5	J11	12	IO	166	2.6					
J5	J11	13	CLK2	X	X					CLK2
J5	J11	14	IO	72	6.12					
J5	J11	15	IO	45	2.13					
J5	J11	16	IO	169	3.2					
J5	J11	17	IO	64	4.0					
J5	J11	18	IO	69	4.5					
J7	J1	1	RESETn							RESETn
J7	J1	2	3.3V							3.3V Board Power
J7	J1	3	5V							USB PWR
J7	J1	4	GND							GND
J7	J1	5	GND							GND
J7	J1	6	NC							Not connected
J8	J2	1	IO	108	7.4					
J8	J2	2	ADC4	X	?			4		ADC
J8	J2	3	IO	111	7.7					
J8	J2	4	ADC2	X	?			2		ADC
J8	J2	5	ADC1	X	?			1		ADC
J8	J2	6	ADC0	X	?			0	1	ADC or DAC
J9	J3	1	5V							USB PWR
J9	J3	2	DM							USB0_DM
J9	J3	3	DP							USB0_DP
J9	J3	4	ID							USB0_ID
J9	J3	5	GND							GND
J10/17	J4/17	1	IO	174	4.10					Copy J3-1
J10/17	J4/17	2	IO	73	5.0					Copy J4-1
J10/17	J4/17	3	IO	4	1.0					Copy J1-1
J10/17	J4/17	4	IO	74	5.1					Copy J5-1
J10/17	J4/17	5	IO	8	1.1					Copy J2-1; Pulled up during reset
J10/17	J4/17	6	IO	9	1.2					Copy J2-2; Pulled down during reset
J10/17	J4/17	7	GND	X	X					GND
J10/17	J4/17	8	IO	7	2.7					Copy J2-11; Pulled down during reset. Connected to LED and ISP BOOT pin
J11/12	J5/12	1	RXD0	161	2.1	0				RXD0, UART0; Copy J3-9
J11/12	J5/12	2	TXD0	160	2.0		0			TXD0, UART0; Copy J3-8
J11/12	J5/12	3	IO	70	4.6					Copy J2-15
J11/12	J5/12	4	CLKIN	X	4.7					CLKIN
J11/12	J5/12	5	IO	172	4.8					Copy J4-10
J11/12	J5/12	6	IO	0	0.0					Copy J1-3
J11/12	J5/12	7	IO	173	4.9					Copy J2-6
J11/12	J5/12	8	IO	1	0.1					Copy J1-2

J13	J13	1	GND							
J13	J13	2	GND							
J13	J13	3	GND							
J13	J13	4	GND							
J14	J14	1	3.3V							
J14	J14	2	3.3V							
J14	J14	3	3.3V							
J14	J14	4	3.3V							
J15	J15	1	RESETn							DONGLE PORT
J15	J15	2	TXD0				0			DONGLE PORT
J15	J15	3	RXD0			0				DONGLE PORT
J15	J15	4	5Vusb							DONGLE PORT
J15	J15	5	BOOT							DONGLE PORT
J15	J15	6	GND							DONGLE PORT
J16	J16	1	AGND							
J16	J16	2	IO	109	7.5					TYP0 DOC says IO(122), should be IO(109)
J16	J16	3	ADC7	X	?			7		ADC
J16	J16	4	ADC3	X	?			3		ADC
J16	J16	5	ADC6	X	?			6		ADC
J16	J16	6	ADC5	X	?			5		ADC
J16	J16	7	IO	65	4.1					
J16	J16	8	IO	67	4.3					